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NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE6890 uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

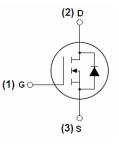
- $V_{DS} = 68V, I_D = 90A$ $R_{DS(ON)} < 7.5mΩ @ V_{GS} = 10V$ (Typ:6.5mΩ)
- Special process technology for high ESD capability
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation

Application

- Power switching application
- Hard switched and High frequency circuits
- Uninterruptible power supply

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100% AVds TESTED!



Schematic diagram



Marking and pin assignment



TO-220-3L top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE6890	NCE6890	TO-220-3L	-	-	-

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	68	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	90	Α
Drain Current-Continuous(T _C =100°C)	I _D (100℃)	63	Α
Pulsed Drain Current	I _{DM}	320	Α
Maximum Power Dissipation	P _D	130	W
Derating factor		0.86	W/℃



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Parameter	Symbol	Limit	Unit
Single pulse avalanche energy (Note 5)	E _{AS}	380	mJ
Operating Junction and Storage Temperature Range	T_J, T_STG	-55 To 175	$^{\circ}\!\mathbb{C}$

Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	R _{θJc}	1.15	°C/W
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Electrical Characteristics (TC=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	<u>.</u>					
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	68	73	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =68V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)			•			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	2	3	4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =30A	-	6.5	7.5	mΩ
Forward Transconductance	g FS	V _{DS} =10V,I _D =20A	20	-	-	S
Dynamic Characteristics (Note4)	<u> </u>		•			
Input Capacitance	C _{lss}	\/ 20\/\/ 0\/	-	3300	-	PF
Output Capacitance	C _{oss}	V_{DS} =30V, V_{GS} =0V, F=1.0MHz	-	450	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.UIVIHZ	-	170	-	PF
Switching Characteristics (Note 4)	<u>.</u>					
Turn-on Delay Time	t _{d(on)}		-	15	-	nS
Turn-on Rise Time	t _r	V _{DD} =30V,I _D =30A	-	94	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10V, R_{GEN} =6 Ω	-	46	-	nS
Turn-Off Fall Time	t _f			32	-	nS
Total Gate Charge	Qg	V -20VI -20A	-	35	-	nC
Gate-Source Charge	Q _{gs}	V_{DS} =30V, I_{D} =20A, V_{GS} =10V	-	11	-	nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	9	-	nC
Drain-Source Diode Characteristics	<u>.</u>					
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =90A	-	-	1.2	V
Diode Forward Current (Note 2)	Is	-	-	-	90	Α
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF =90A	-	78	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)		51	-	nC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				y LS+LD)

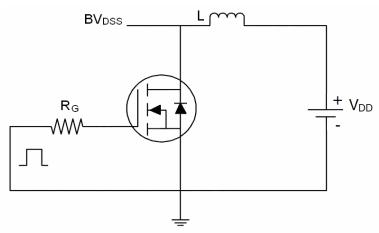
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- **4.** Guaranteed by design, not subject to production
- **5.** E_{AS} condition:Tj=25 $^{\circ}\!\!\mathrm{C}$,V_{DD}=30V,V_G=10V,L=0.5mH,Rg=25 Ω

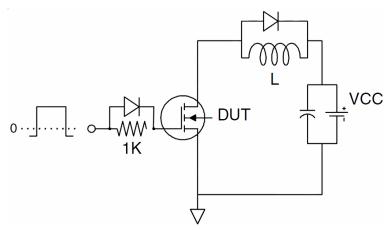


Test Circuit

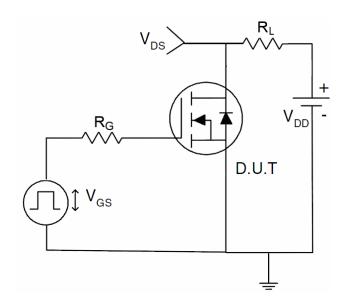
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit



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Typical Electrical and Thermal Characteristics (Curves)

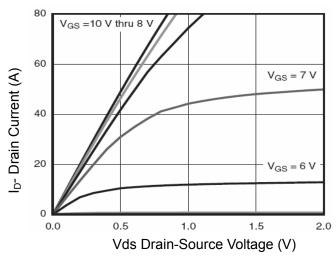


Figure 1 Output Characteristics

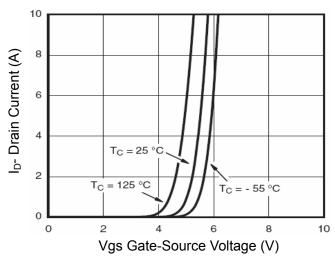


Figure 2 Transfer Characteristics

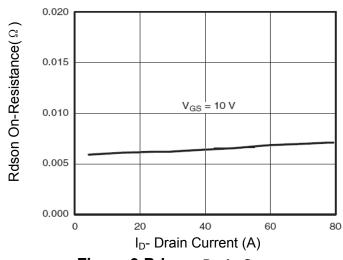


Figure 3 Rdson- Drain Current

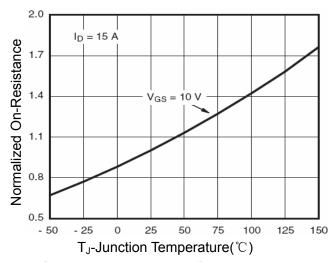


Figure 4 Rdson-JunctionTemperature

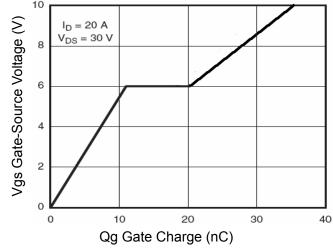


Figure 5 Gate Charge

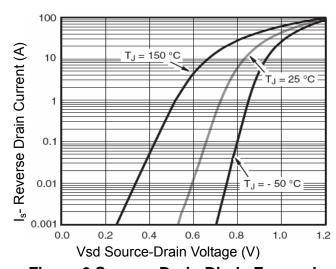


Figure 6 Source- Drain Diode Forward



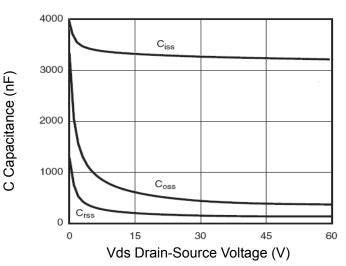


Figure 7 Capacitance vs Vds

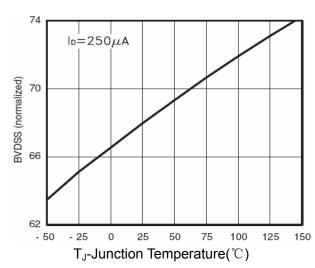


Figure 9 BV_{DSS} vs Junction Temperature

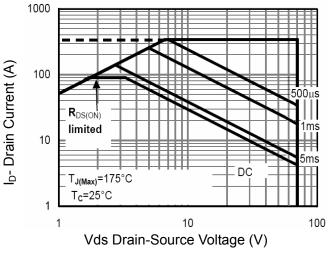


Figure 8 Safe Operation Area

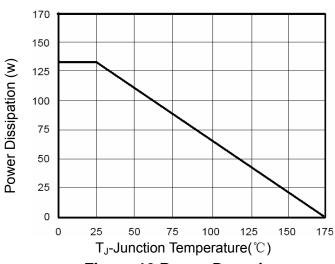
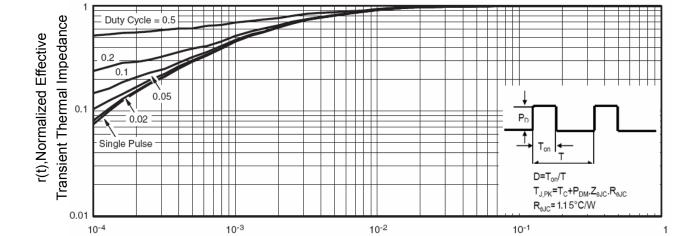


Figure 10 Power De-rating



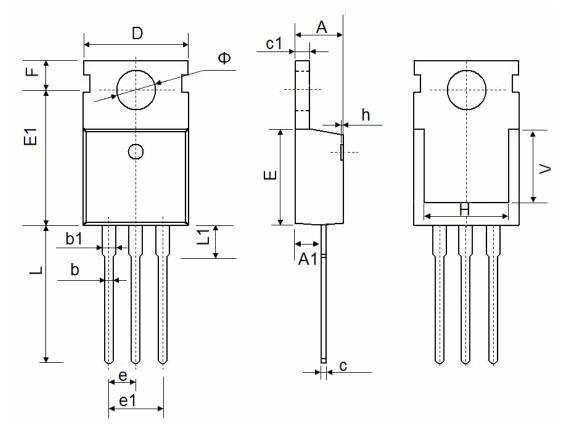
Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance

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TO-220-3L Package Information



Comple of	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	4.400	4.600	0.173	0.181	
A1	2.250	2.550	0.089	0.100	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
С	0.330	0.650	0.013	0.026	
c1	1.200	1.400	0.047	0.055	
D	9.910	10.250	0.390	0.404	
E	8.9500	9.750	0.352	0.384	
E1	12.650	12.950	0.498	0.510	
е	2.540	TYP.	0.100 TYP.		
e1	4.980	5.180	0.196	0.204	
F	2.650	2.950	0.104	0.116	
Н	7.900	8.100	0.311	0.319	
h	0.000	0.300	0.000	0.012	
L	12.900	13.400	0.508	0.528	
L1	2.850	3.250	0.112	0.128	
V	7.500	REF.	0.295 REF.		
Ф	3.400	3.800	0.134	0.150	



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